



co/c

Docket No. 740819-566

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of )  
Yoshihisa KATO et al. )  
Serial No. -09/886,972 )  
Patent No 6,771,530 ) Art Unit: 2818  
Issued: August 3, 2004 ) Examiner: Ly D. PHAM  
Filed: June 25, 2001 ) Confirmation No. 7056

For: SEMICONDUCTOR MEMORY AND METHOD FOR DRIVING THE SAME

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents  
Alexandria, VA 22313-1450

**Certificate**  
**FEB 24 2005**  
**of Correction**

Sir:

In reviewing the above-identified patent, printing errors were discovered therein requiring correction in order to conform the Official Record in the application.


The errors noted are set forth on the attached copy of Form PTO-1050 (Rev. 3-82) in the manner required by the Commissioner's Notice.

Specifically, upon reviewing the front page of the patent, please correct the following:

In the claims, claim 2, column 7, line 2, "ferro electric" should read -- **ferroelectric** --

The change requested herein occurred as a result of printing the Letters Patent and the Certificate should be issued without expense under Rule 322 of the Rules of Practice. Accordingly, applicants request issuance of the Certificate of Correction.

Respectfully submitted,

  
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DRS/eoc

W633960.1

**MAR 7 2005**

UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO : 6,771,530

DATED : August 3, 2004

INVENTOR(S) : Yoshihisa KATO et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the claims, claim 2, column 7, line 2, "ferro electric" should read - - ferroelectric - -

MAILING ADDRESS OF SENDER:

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PATENT NO. 6,771,530

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P.O. Box 1450  
Alexandria, VA 22313-1450

UNITED STATES PATENT AND TRADEMARK OFFICE

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